1/2

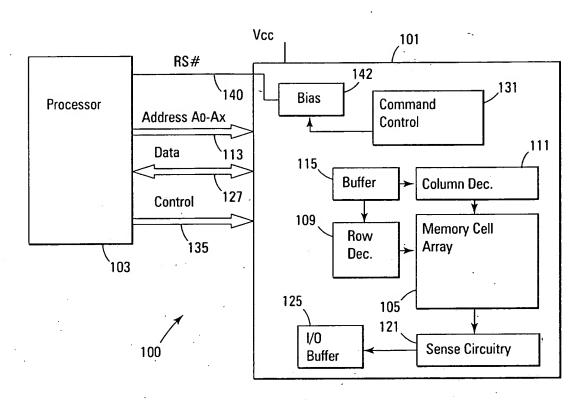
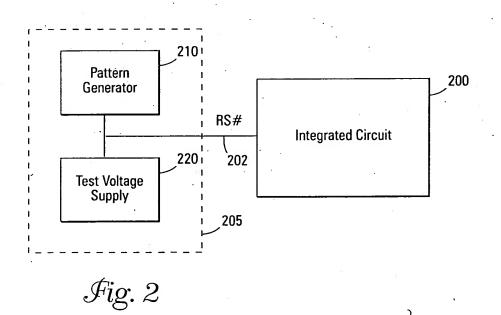
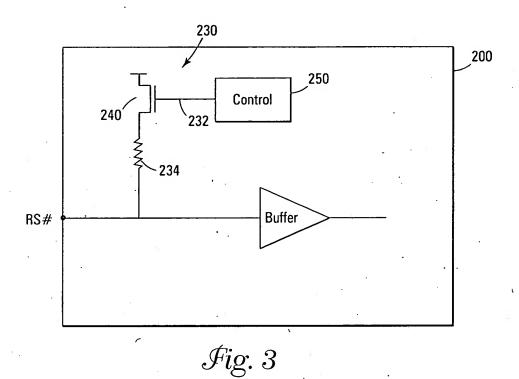


Fig. 1





Control

RS#

Buffer

Fig. 4